

**Department of CSE**

**CSE209 Lab**

**Course Name: Electrical Circuits**

**Course Code: CSE209**

**Section No: 2**

**Experiment No: 03**

**Name of the Experiment:** Bias Point Detail Analysis of DC Circuit with Independent Sources Using PSpice Schematics.

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Student’s Name: Md Abdul Ahad Rifat

Student’s ID: 2020-1-60-215

**Submitted to**

Rashedul Amin Tuhin

Senior Lecturer

Department of Computer Science and Engineering

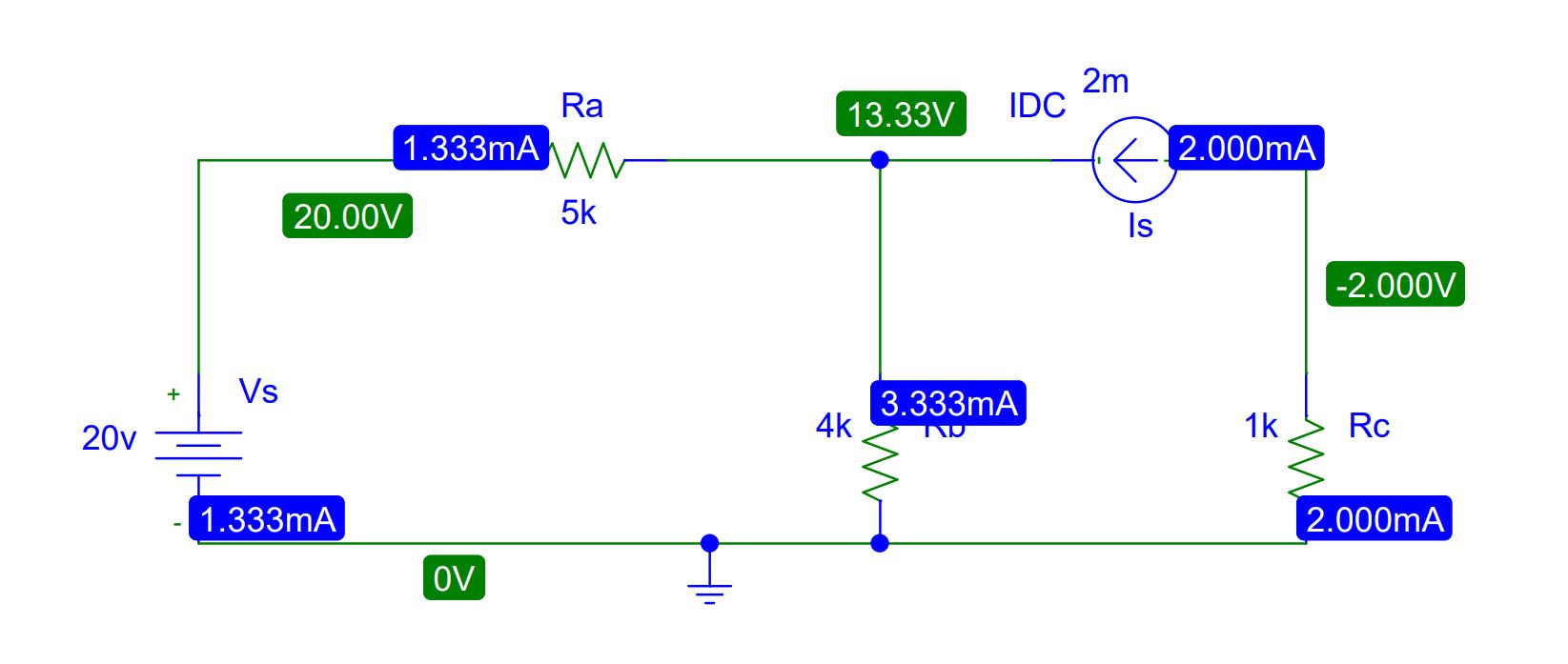
East West University

**Objectives:**

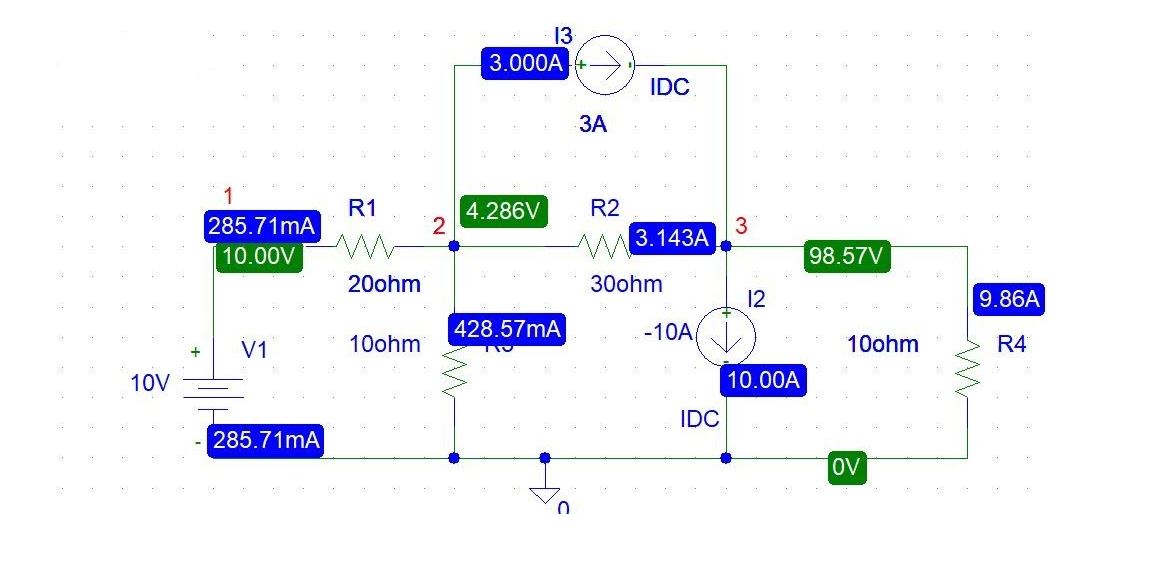
1. To learn fundamentals of PSpice.

2. To analyze Bias Point Detail of DC circuit using PSpice Schematics.

**Circuit Diagram(s):**



**Figure 1.Circuit Diagram for Experiment 3**



**Figure 2.Circuit Diagram for Experiment 3**

**Post-Lab Report Questions and Answers:**

1. Theoretically calculate all the currents and the voltages for the circuit shown in Figure 2.

**Answer:**

Applying KCL at node 2,

Applying KCL at node 3,

Solving equation 1 and 2 we get,

1. Compare the theoretical solution of the circuit shown in Figure 2 with the solutions obtained from PSpice.

**Answer:** There has been no change in theoretical calculation value and Pspice simulation value in Figure 2.

**Conclusion:**

In experiment 3 in using PSpice use is nothing new for us. We already know how to use PSpice software since in experiment no 1. In this experiment, we identify the value of Voltage and Current using PSpice simulation software also we theoretically calculate this value using KCL. And there is no discrepancy between the theoretical and simulated value because this is not the actual lab. So this experiment main theme is how to use PSpice software to identify Bias Point Detail of DC circuit using PSpice Schematics.